

# Affine Input/State/Output Representations of State/Signal Systems

Damir Arov

South-Ukrainian Pedagogical University

Olof Staffans

Åbo Akademi University

<http://www.abo.fi/~staffans>

# Summary

- Discrete time-invariant i/s/o systems
- State/signal systems
- Affine representations of state/signal systems
- Generalized transfer functions
- Coprime Representations
- Extensions

# Discrete Time-Invariant I/S/O Systems

## Discrete Time-Invariant I/S/O System

Linear discrete-time-invariant systems are typically modeled as i/s/o (input/state/output) systems of the type

$$\begin{aligned}x(n+1) &= Ax(n) + Bu(n), & n \in \mathbb{Z}^+, & \quad x(0) = x_0, \\y(n) &= Cx(n) + Du(n), & n \in \mathbb{Z}^+.\end{aligned}\tag{1}$$

Here  $\mathbb{Z}^+ = \{0, 1, 2, \dots\}$  and  $A, B, C, D$ , are bounded operators.

## Discrete Time-Invariant I/S/O System

Linear discrete-time-invariant systems are typically modeled as i/s/o (input/state/output) systems of the type

$$\begin{aligned}x(n+1) &= Ax(n) + Bu(n), & n \in \mathbb{Z}^+, & \quad x(0) = x_0, \\y(n) &= Cx(n) + Du(n), & n \in \mathbb{Z}^+.\end{aligned}\tag{1}$$

Here  $\mathbb{Z}^+ = \{0, 1, 2, \dots\}$  and  $A, B, C, D$ , are bounded operators.

$u(n) \in \mathcal{U}$  = the **input space**,  
 $x(n) \in \mathcal{X}$  = the **state space**,  
 $y(n) \in \mathcal{Y}$  = the **output space** (all Hilbert spaces).

## Discrete Time-Invariant I/S/O System

Linear discrete-time-invariant systems are typically modeled as i/s/o (input/state/output) systems of the type

$$\begin{aligned}x(n+1) &= Ax(n) + Bu(n), & n \in \mathbb{Z}^+, & \quad x(0) = x_0, \\y(n) &= Cx(n) + Du(n), & n \in \mathbb{Z}^+.\end{aligned}\tag{1}$$

Here  $\mathbb{Z}^+ = \{0, 1, 2, \dots\}$  and  $A, B, C, D$ , are bounded operators.

$u(n) \in \mathcal{U}$  = the **input space**,  
 $x(n) \in \mathcal{X}$  = the **state space**,  
 $y(n) \in \mathcal{Y}$  = the **output space** (all Hilbert spaces).

By a **trajectory** of this system we mean a triple of sequences  $(u, x, y)$  satisfying (1).

We denote this system by  $\Sigma_{i/s/o} = ([\begin{smallmatrix} A & B \\ C & D \end{smallmatrix}]; \mathcal{X}, \mathcal{U}, \mathcal{Y})$ .

# Frequency Domain Interpretation

## Frequency Domain Interpretation

The  $Z$ -transform of a sequence  $\{x(n)\}_{n=0}^{\infty}$  is given by  $\hat{x}(z) = \sum_{n=0}^{\infty} x(n)z^n$ . Taking  $Z$ -transforms in (1) and solving for  $\hat{x}(z)$  we get the frequency domain i/s/o equations

$$\begin{aligned}\hat{x}(z) &= \mathfrak{A}(z)x_0 + \mathfrak{B}(z)\hat{u}(z), \\ \hat{y}(z) &= \mathfrak{C}(z)x_0 + \mathfrak{D}(z)\hat{u}(z), \quad \text{for small } |z|.\end{aligned}\tag{2}$$

where

$$\begin{bmatrix} \mathfrak{A}(z) & \mathfrak{B}(z) \\ \mathfrak{C}(z) & \mathfrak{D}(z) \end{bmatrix} = \begin{bmatrix} (1_{\mathcal{X}} - zA)^{-1} & z(1_{\mathcal{X}} - zA)^{-1}B \\ C(1_{\mathcal{X}} - zA)^{-1} & zC(1_{\mathcal{X}} - zA)^{-1}B + D \end{bmatrix}\tag{3}$$

is the four block transfer function of  $\Sigma$  corresponding to the i/o decomposition  $\mathcal{W} = \mathcal{Y} \dot{+} \mathcal{U}$ . The bottom right block  $\mathfrak{D}(z) = zC(1_{\mathcal{X}} - zA)^{-1}B + D$  is the i/o transfer function of  $\Sigma$  corresponding to this i/o decomposition.

## Frequency Domain Interpretation

The  $Z$ -transform of a sequence  $\{x(n)\}_{n=0}^{\infty}$  is given by  $\hat{x}(z) = \sum_{n=0}^{\infty} x(n)z^n$ . Taking  $Z$ -transforms in (1) and solving for  $\hat{x}(z)$  we get the **frequency domain i/s/o equations**

$$\begin{aligned}\hat{x}(z) &= \mathfrak{A}(z)x_0 + \mathfrak{B}(z)\hat{u}(z), \\ \hat{y}(z) &= \mathfrak{C}(z)x_0 + \mathfrak{D}(z)\hat{u}(z), \quad \text{for small } |z|.\end{aligned}\tag{2}$$

where

$$\begin{bmatrix} \mathfrak{A}(z) & \mathfrak{B}(z) \\ \mathfrak{C}(z) & \mathfrak{D}(z) \end{bmatrix} = \begin{bmatrix} (1_{\mathcal{X}} - zA)^{-1} & z(1_{\mathcal{X}} - zA)^{-1}B \\ C(1_{\mathcal{X}} - zA)^{-1} & zC(1_{\mathcal{X}} - zA)^{-1}B + D \end{bmatrix}\tag{3}$$

is the **four block transfer function** of  $\Sigma$  corresponding to the i/o decomposition  $\mathcal{W} = \mathcal{Y} \dot{+} \mathcal{U}$ . The bottom right block  $\mathfrak{D}(z) = zC(1_{\mathcal{X}} - zA)^{-1}B + D$  is the **i/o transfer function** of  $\Sigma$  corresponding to this i/o decomposition.

The natural **domain of definition** is the set  $\Lambda_A$  consisting of those  $z \in \mathbb{C}$  for which  $1_{\mathcal{X}} - zA$  has a bounded inverse (including  $z = \infty$  if  $A$  has a bounded inverse).

## The I/S/O Model is an Idealization

The i/s/o model is an **idealized model** of a true system, with “infinite input impedance and zero output impedance”:

## The I/S/O Model is an Idealization

The i/s/o model is an **idealized model** of a true system, with “infinite input impedance and zero output impedance”:

If we connect two such systems in series, then the second system has no influence on the first system.

In particular, there is no limit on how many inputs that can be connected to an output before the performance degrades (as it always does in practice). **In real life,**

## The I/S/O Model is an Idealization

The i/s/o model is an **idealized model** of a true system, with “infinite input impedance and zero output impedance”:

If we connect two such systems in series, then the second system has no influence on the first system.

In particular, there is no limit on how many inputs that can be connected to an output before the performance degrades (as it always does in practice). **In real life,**

- **every input is also an output**, since it influences the output to which it is connected,
- **every output is also an input**, since the true output depends also on the load.

# The I/S/O Model is an Idealization

The i/s/o model is an **idealized model** of a true system, with “infinite input impedance and zero output impedance”:

If we connect two such systems in series, then the second system has no influence on the first system.

In particular, there is no limit on how many inputs that can be connected to an output before the performance degrades (as it always does in practice). **In real life,**

- **every input is also an output**, since it influences the output to which it is connected,
- **every output is also an input**, since the true output depends also on the load.

One way to avoid this problem is to **ignore the distinction between an input and an output**, and to replace the i/s/o model by a **state/signal model**.

# State/Signal Systems

# State/Signal System: Definition

## State/Signal System: Definition

We start by combining the input space  $\mathcal{U}$  and the output space  $\mathcal{Y}$  into one **signal space**  $\mathcal{W} = \begin{bmatrix} \mathcal{Y} \\ \mathcal{U} \end{bmatrix}$ .

## State/Signal System: Definition

We start by combining the input space  $\mathcal{U}$  and the output space  $\mathcal{Y}$  into one **signal space**  $\mathcal{W} = \begin{bmatrix} \mathcal{Y} \\ \mathcal{U} \end{bmatrix}$ .

A linear discrete time-invariant s/s system  $\Sigma$  is modelled by a system of equations

$$x(n+1) = F \begin{bmatrix} x(n) \\ w(n) \end{bmatrix}, \quad n \in \mathbb{Z}^+, \quad x(0) = x_0, \quad (4)$$

Here  $F$  is a bounded linear operator with a closed domain  $\mathcal{D}(F) \subset \begin{bmatrix} \mathcal{X} \\ \mathcal{W} \end{bmatrix}$  ( $\mathbb{Z}^+ = 0, 1, 2, \dots$ ) and a certain additional property.

## State/Signal System: Definition

We start by combining the input space  $\mathcal{U}$  and the output space  $\mathcal{Y}$  into one **signal space**  $\mathcal{W} = \begin{bmatrix} \mathcal{Y} \\ \mathcal{U} \end{bmatrix}$ .

A linear discrete time-invariant s/s system  $\Sigma$  is modelled by a system of equations

$$x(n+1) = F \begin{bmatrix} x(n) \\ w(n) \end{bmatrix}, \quad n \in \mathbb{Z}^+, \quad x(0) = x_0, \quad (4)$$

Here  $F$  is a bounded linear operator with a closed domain  $\mathcal{D}(F) \subset \begin{bmatrix} \mathcal{X} \\ \mathcal{W} \end{bmatrix}$  ( $\mathbb{Z}^+ = 0, 1, 2, \dots$ ) and a certain additional property.

$x(n) \in \mathcal{X} =$  the **state space** (typically a **Hilbert** space),  
 $w(n) \in \mathcal{W} =$  the **signal space** (typically a **Kreĭn** space).

## State/Signal System: Definition

We start by combining the input space  $\mathcal{U}$  and the output space  $\mathcal{Y}$  into one **signal space**  $\mathcal{W} = \begin{bmatrix} \mathcal{Y} \\ \mathcal{U} \end{bmatrix}$ .

A linear discrete time-invariant s/s system  $\Sigma$  is modelled by a system of equations

$$x(n+1) = F \begin{bmatrix} x(n) \\ w(n) \end{bmatrix}, \quad n \in \mathbb{Z}^+, \quad x(0) = x_0, \quad (4)$$

Here  $F$  is a bounded linear operator with a closed domain  $\mathcal{D}(F) \subset \begin{bmatrix} \mathcal{X} \\ \mathcal{W} \end{bmatrix}$  ( $\mathbb{Z}^+ = 0, 1, 2, \dots$ ) and a certain additional property.

$x(n) \in \mathcal{X} =$  the **state space** (typically a **Hilbert** space),  
 $w(n) \in \mathcal{W} =$  the **signal space** (typically a **Kreĭn** space).

By a **trajectory** of this system we mean a pair of sequences  $(x, w)$  satisfying (4).

## State/Signal System: Definition

We start by combining the input space  $\mathcal{U}$  and the output space  $\mathcal{Y}$  into one **signal space**  $\mathcal{W} = \begin{bmatrix} \mathcal{Y} \\ \mathcal{U} \end{bmatrix}$ .

A linear discrete time-invariant s/s system  $\Sigma$  is modelled by a system of equations

$$x(n+1) = F \begin{bmatrix} x(n) \\ w(n) \end{bmatrix}, \quad n \in \mathbb{Z}^+, \quad x(0) = x_0, \quad (4)$$

Here  $F$  is a bounded linear operator with a closed domain  $\mathcal{D}(F) \subset \begin{bmatrix} \mathcal{X} \\ \mathcal{W} \end{bmatrix}$  ( $\mathbb{Z}^+ = 0, 1, 2, \dots$ ) and a certain additional property.

$x(n) \in \mathcal{X} =$  the **state space** (typically a **Hilbert** space),  
 $w(n) \in \mathcal{W} =$  the **signal space** (typically a **Kreĭn** space).

By a **trajectory** of this system we mean a pair of sequences  $(x, w)$  satisfying (4).

In the case of an i/s/o system we take  $w = \begin{bmatrix} y \\ u \end{bmatrix}$ ,  $F \begin{bmatrix} x \\ u \end{bmatrix} = Ax + Bu$ , and

$$\mathcal{D}(F) = \left\{ \begin{bmatrix} x \\ u \\ y \end{bmatrix} \mid y = Cx + Du \right\}.$$

## Additional Property of $F$

We require  $F$  to have the following property:

## Additional Property of $F$

We require  $F$  to have the following property:

- (i) Every  $x_0 \in \mathcal{X}$  is the initial state of some trajectory.

## Additional Property of $F$

We require  $F$  to have the following property:

- (i) Every  $x_0 \in \mathcal{X}$  is the initial state of some trajectory.

It follows from (4) that moreover

- (ii) A trajectory  $(x, w)$  is uniquely determined by the initial state  $x_0$  and the signal part  $w$ .

## Additional Property of $F$

We require  $F$  to have the following property:

- (i) Every  $x_0 \in \mathcal{X}$  is the initial state of some trajectory.

It follows from (4) that moreover

- (ii) A trajectory  $(x, w)$  is uniquely determined by the initial state  $x_0$  and the signal part  $w$ .
- (iii) The trajectory  $(x, w)$  depends continuously on the initial state  $x_0$  and the signal part  $w$ .

# Admissible I/O Decompositions

## Admissible I/O Decompositions

Let  $\mathcal{W} = \mathcal{Y} \dot{+} \mathcal{U}$  be a direct sum decomposition of the signal space  $\mathcal{W}$ . We call this decomposition **an admissible i/o decomposition** of  $\mathcal{W}$  for the s/s system  $\Sigma$  (with  $\mathcal{U}$  as input space and  $\mathcal{Y}$  as output space) if the s/s equation

$$x(n+1) = F \begin{bmatrix} x(n) \\ w(n) \end{bmatrix}, \quad n \in \mathbb{Z}^+, \quad x(0) = x_0, \quad (4)$$

can be written in i/s/o form (for some bounded linear operators  $A, B, C, D$ )

$$\begin{aligned} x(n+1) &= Ax(n) + Bu(n), & n \in \mathbb{Z}^+, & \quad x(0) = x_0, \\ y(n) &= Cx(n) + Du(n), & n \in \mathbb{Z}^+. & \end{aligned} \quad (1)$$

where  $u(n)$  and  $y(n)$  are the projections of  $w(n)$  onto  $\mathcal{U}$  and  $\mathcal{Y}$ , respectively.

## Admissible I/O Decompositions

Let  $\mathcal{W} = \mathcal{Y} \dot{+} \mathcal{U}$  be a direct sum decomposition of the signal space  $\mathcal{W}$ . We call this decomposition **an admissible i/o decomposition** of  $\mathcal{W}$  for the s/s system  $\Sigma$  (with  $\mathcal{U}$  as input space and  $\mathcal{Y}$  as output space) if the s/s equation

$$x(n+1) = F \begin{bmatrix} x(n) \\ w(n) \end{bmatrix}, \quad n \in \mathbb{Z}^+, \quad x(0) = x_0, \quad (4)$$

can be written in i/s/o form (for some bounded linear operators  $A, B, C, D$ )

$$\begin{aligned} x(n+1) &= Ax(n) + Bu(n), & n \in \mathbb{Z}^+, & \quad x(0) = x_0, \\ y(n) &= Cx(n) + Du(n), & n \in \mathbb{Z}^+. & \end{aligned} \quad (1)$$

where  $u(n)$  and  $y(n)$  are the projections of  $w(n)$  onto  $\mathcal{U}$  and  $\mathcal{Y}$ , respectively.

We then call  $\Sigma_{i/s/o} = ([\begin{smallmatrix} A & B \\ C & D \end{smallmatrix}]; \mathcal{X}, \mathcal{U}, \mathcal{Y})$  **the i/s/o representation of  $\Sigma$**  corresponding to the decomposition  $\mathcal{W} = \mathcal{Y} \dot{+} \mathcal{U}$ .

# Affine Representations of State/Signal Systems

## Affine Representations of $\Sigma$

Not every i/o decomposition of  $\mathcal{W}$  is admissible.

To be able to treat also the nonadmissible case we introduce **right and left affine (= fractional) generalizations** of the notions of **i/s/o representations** and their **transfer functions**.

These are defined for arbitrary i/o decompositions  $\mathcal{W} = \mathcal{Y} \dot{+} \mathcal{U}$  (also nonadmissible ones).

## Right Affine Representations

By a **right affine i/s/o representation** of  $\Sigma$  we mean an i/s/o system  $\Sigma_{i/s/o}^r$  generated by the system of equations (a **driving variable representation**)

$$\begin{aligned}x(n+1) &= A'x(n) + B'\ell(n), \\y(n) &= C'_y x(n) + D'_y \ell(n), \\u(n) &= C'_u x(n) + D'_u \ell(n), \quad n \in \mathbb{Z}^+, \ell(n) \in \mathcal{L}\end{aligned}$$

(where the new input space  $\mathcal{L}$  is an auxiliary Hilbert space) with the following two properties:

- 1)  $D' = \begin{bmatrix} D'_y \\ D'_u \end{bmatrix}$  has a bounded left-inverse,
- 2)  $\left( x(\cdot), \begin{bmatrix} y(\cdot) \\ u(\cdot) \end{bmatrix} \right)$  is a trajectory of  $\Sigma$  if and only if  $\left( x(\cdot), \ell(\cdot), \begin{bmatrix} y(\cdot) \\ u(\cdot) \end{bmatrix} \right)$  is a trajectory of  $\Sigma_{i/s/o}^r$  for **some** sequence  $\ell(\cdot)$  with values in  $\mathcal{L}$ .

## Left Affine Representations

By a **left affine i/s/o representation** of  $\Sigma$  we mean an i/s/o system  $\Sigma_{i/s/o}^l$  generated by the system of equations (an **output nulling representation**)

$$\begin{aligned}x(n+1) &= A''x(n) + B''_y y(n) + B''_u u(n), \\e(n) &= C''x(n) + D''_y y(n) + D''_u u(n) = 0, \quad n \in \mathbb{Z}^+\end{aligned}$$

(where the new output space  $\mathcal{K}$  is another auxiliary Hilbert space) with the following two properties:

- 1)  $D'' = \begin{bmatrix} D''_y & D''_u \end{bmatrix}$  has a bounded right-inverse,
- 2)  $\left( x(\cdot), \begin{bmatrix} y(\cdot) \\ u(\cdot) \end{bmatrix} \right)$  is a trajectory of  $\Sigma$  if and only if  $\left( x(\cdot), \begin{bmatrix} y(\cdot) \\ u(\cdot) \end{bmatrix}, 0 \right)$  is a trajectory of  $\Sigma_{i/s/o}^l$  (i.e., the output is identically zero in  $\mathcal{K}$ ).

## Right and Left Affine Four Block Transfer Functions

The frequency domain versions of these representations are

$$\begin{aligned}\hat{x}(z) &= \mathfrak{A}'(z)x_0 + \mathfrak{B}'(z)\hat{\ell}(z), \\ \hat{y}(z) &= \mathfrak{C}'_y(z)x_0 + \mathfrak{D}'_y(z)\hat{\ell}(z), \\ \hat{u}(z) &= \mathfrak{C}'_u(z)x_0 + \mathfrak{D}'_u(z)\hat{\ell}(z),\end{aligned}\quad \text{for small } |z|, \hat{\ell}(z) \text{ is free.} \tag{5}$$

## Right and Left Affine Four Block Transfer Functions

The frequency domain versions of these representations are

$$\begin{aligned}\hat{x}(z) &= \mathfrak{A}'(z)x_0 + \mathfrak{B}'(z)\hat{\ell}(z), \\ \hat{y}(z) &= \mathfrak{C}'_y(z)x_0 + \mathfrak{D}'_y(z)\hat{\ell}(z), \\ \hat{u}(z) &= \mathfrak{C}'_u(z)x_0 + \mathfrak{D}'_u(z)\hat{\ell}(z), \quad \text{for small } |z|, \hat{\ell}(z) \text{ is free.}\end{aligned}\tag{5}$$

$$\begin{aligned}\hat{x}(z) &= \mathfrak{A}''(z)x_0 + \mathfrak{B}''_y(z)\hat{y}(z) + \mathfrak{B}''_u(z)\hat{u}(z), \\ \hat{e}(z) &= \mathfrak{C}''(z)x_0 + \mathfrak{D}''_y(z)\hat{y}(z) + \mathfrak{D}''_u(z)\hat{u}(z) = 0 \quad \text{for small } |z|.\end{aligned}\tag{6}$$

## Right and Left Affine Four Block Transfer Functions

The frequency domain versions of these representations are

$$\begin{aligned}
 \hat{x}(z) &= \mathfrak{A}'(z)x_0 + \mathfrak{B}'(z)\hat{\ell}(z), \\
 \hat{y}(z) &= \mathfrak{C}'_{\mathcal{Y}}(z)x_0 + \mathfrak{D}'_{\mathcal{Y}}(z)\hat{\ell}(z), \\
 \hat{u}(z) &= \mathfrak{C}'_{\mathcal{U}}(z)x_0 + \mathfrak{D}'_{\mathcal{U}}(z)\hat{\ell}(z), \quad \text{for small } |z|, \hat{\ell}(z) \text{ is free.}
 \end{aligned} \tag{5}$$

$$\begin{aligned}
 \hat{x}(z) &= \mathfrak{A}''(z)x_0 + \mathfrak{B}''_{\mathcal{Y}}(z)\hat{y}(z) + \mathfrak{B}''_{\mathcal{U}}(z)\hat{u}(z), \\
 \hat{e}(z) &= \mathfrak{C}''(z)x_0 + \mathfrak{D}''_{\mathcal{Y}}(z)\hat{y}(z) + \mathfrak{D}''_{\mathcal{U}}(z)\hat{u}(z) = 0 \quad \text{for small } |z|.
 \end{aligned} \tag{6}$$

The corresponding transfer functions are called the **right, respectively left affine transfer functions** of  $\Sigma$  corresponding to the (possibly non-admissible) i/o decomposition  $\mathcal{W} = \mathcal{Y} \dot{+} \mathcal{U}$ . Note, in particular, that the right and left affine i/o transfer functions are now **decomposed** into  $\mathfrak{D}' = \begin{bmatrix} \mathfrak{D}'_{\mathcal{Y}} \\ \mathfrak{D}'_{\mathcal{U}} \end{bmatrix}$  and  $\mathfrak{D}'' = \begin{bmatrix} \mathfrak{D}''_{\mathcal{Y}} & \mathfrak{D}''_{\mathcal{U}} \end{bmatrix}$ .

# Generalized Transfer Functions

## Generalized Right Transfer Function

Solving (5) for  $\hat{x}(z)$  and  $\hat{y}(z)$  we get the following **generalized right four block transfer function** with input space  $\mathcal{U}$  and output space  $\mathcal{Y}$

$$\begin{bmatrix} \mathfrak{A}_r(z) & \mathfrak{B}_r(z) \\ \mathfrak{C}_r(z) & \mathfrak{D}_r(z) \end{bmatrix} = \begin{bmatrix} \mathfrak{A}'(z) & \mathfrak{B}'(z) \\ \mathfrak{C}'_{\mathcal{Y}}(z) & \mathfrak{D}'_{\mathcal{Y}}(z) \end{bmatrix} \begin{bmatrix} 1_{\mathcal{X}} & 0 \\ \mathfrak{C}'_{\mathcal{U}}(z) & \mathfrak{D}'_{\mathcal{U}}(z) \end{bmatrix}^{-1}, \quad (7)$$

defined for all  $z$  in the set

$$\Omega(\Sigma_{i/s/o}^r) := \{z \in \Lambda_{A'} \mid \mathfrak{D}'_{\mathcal{U}}(z) \text{ has a bounded inverse}\}.$$

In particular, the **generalized right i/o transfer function** is given by

$$\mathfrak{D}_r(z) = \mathfrak{D}'_{\mathcal{Y}}(z)\mathfrak{D}'_{\mathcal{U}}(z)^{-1}.$$

## Generalized Right Transfer Function

Solving (5) for  $\hat{x}(z)$  and  $\hat{y}(z)$  we get the following **generalized right four block transfer function** with input space  $\mathcal{U}$  and output space  $\mathcal{Y}$

$$\begin{bmatrix} \mathfrak{A}_r(z) & \mathfrak{B}_r(z) \\ \mathfrak{C}_r(z) & \mathfrak{D}_r(z) \end{bmatrix} = \begin{bmatrix} \mathfrak{A}'(z) & \mathfrak{B}'(z) \\ \mathfrak{C}'_{\mathcal{Y}}(z) & \mathfrak{D}'_{\mathcal{Y}}(z) \end{bmatrix} \begin{bmatrix} 1_{\mathcal{X}} & 0 \\ \mathfrak{C}'_{\mathcal{U}}(z) & \mathfrak{D}'_{\mathcal{U}}(z) \end{bmatrix}^{-1}, \quad (7)$$

defined for all  $z$  in the set

$$\Omega(\Sigma_{i/s/o}^r) := \{z \in \Lambda_{A'} \mid \mathfrak{D}'_{\mathcal{U}}(z) \text{ has a bounded inverse}\}.$$

In particular, the **generalized right i/o transfer function** is given by

$$\mathfrak{D}_r(z) = \mathfrak{D}'_{\mathcal{Y}}(z)\mathfrak{D}'_{\mathcal{U}}(z)^{-1}.$$

By varying the representation we can thus define  $\begin{bmatrix} \mathfrak{A}_r(z) & \mathfrak{B}_r(z) \\ \mathfrak{C}_r(z) & \mathfrak{D}_r(z) \end{bmatrix}$  for all  $z$  in the set

$$\Omega^r(\Sigma; \mathcal{U}, \mathcal{Y}) := \text{the union of the above sets } \Omega(\Sigma_{i/s/o}^r).$$

## Generalized Left Transfer Function

Solving (6) for  $\hat{x}(z)$  and  $\hat{y}(z)$  we get the following **generalized left four block transfer function** with input space  $\mathcal{U}$  and output space  $\mathcal{Y}$

$$\begin{bmatrix} \mathfrak{A}_l(z) & \mathfrak{B}_l(z) \\ \mathfrak{C}_l(z) & \mathfrak{D}_l(z) \end{bmatrix} = \begin{bmatrix} 1_{\mathcal{X}} & -\mathfrak{B}''_{\mathcal{Y}}(z) \\ 0 & -\mathfrak{D}''_{\mathcal{Y}}(z) \end{bmatrix}^{-1} \begin{bmatrix} \mathfrak{A}''(z) & \mathfrak{B}''_{\mathcal{U}}(z) \\ \mathfrak{C}''(z) & \mathfrak{D}''_{\mathcal{U}}(z) \end{bmatrix}. \quad (8)$$

defined for all  $z$  in the set

$$\Omega(\Sigma_{i/s/o}^l) := \{z \in \Lambda_{A''} \mid \mathfrak{D}''_{\mathcal{Y}}(z) \text{ has a bounded inverse}\}.$$

In particular, the **generalized left i/o transfer function** is given by

$$\mathfrak{D}_l(z) = -\mathfrak{D}''_{\mathcal{Y}}(z)^{-1} \mathfrak{D}''_{\mathcal{U}}(z).$$

## Generalized Left Transfer Function

Solving (6) for  $\hat{x}(z)$  and  $\hat{y}(z)$  we get the following **generalized left four block transfer function** with input space  $\mathcal{U}$  and output space  $\mathcal{Y}$

$$\begin{bmatrix} \mathfrak{A}_l(z) & \mathfrak{B}_l(z) \\ \mathfrak{C}_l(z) & \mathfrak{D}_l(z) \end{bmatrix} = \begin{bmatrix} 1_{\mathcal{X}} & -\mathfrak{B}''_{\mathcal{Y}}(z) \\ 0 & -\mathfrak{D}''_{\mathcal{Y}}(z) \end{bmatrix}^{-1} \begin{bmatrix} \mathfrak{A}''(z) & \mathfrak{B}''_{\mathcal{U}}(z) \\ \mathfrak{C}''(z) & \mathfrak{D}''_{\mathcal{U}}(z) \end{bmatrix}. \quad (8)$$

defined for all  $z$  in the set

$$\Omega(\Sigma^l_{i/s/o}) := \{z \in \Lambda_{A''} \mid \mathfrak{D}''_{\mathcal{Y}}(z) \text{ has a bounded inverse}\}.$$

In particular, the **generalized left i/o transfer function** is given by

$$\mathfrak{D}_l(z) = -\mathfrak{D}''_{\mathcal{Y}}(z)^{-1} \mathfrak{D}''_{\mathcal{U}}(z).$$

By varying the representation we can thus define  $\begin{bmatrix} \mathfrak{A}_l(z) & \mathfrak{B}_l(z) \\ \mathfrak{C}_l(z) & \mathfrak{D}_l(z) \end{bmatrix}$  for all  $z$  in the set

$$\Omega^l(\Sigma; \mathcal{U}, \mathcal{Y}) := \text{the union of the above sets } \Omega(\Sigma^l_{i/s/o}).$$

# Domains of Right and Left Generalized Transfer Functions

Thus,

- $z \in \Omega^r(\Sigma; \mathcal{U}, \mathcal{Y})$  if there exists at least one right affine representation  $\Sigma_{i/s/o}^r$  for which formula (7) defining  $\begin{bmatrix} \mathfrak{A}_r(z) & \mathfrak{B}_r(z) \\ \mathfrak{C}_r(z) & \mathfrak{D}_r(z) \end{bmatrix}$  makes sense.
- $z \in \Omega^l(\Sigma; \mathcal{U}, \mathcal{Y})$  if there exists at least one left affine representation  $\Sigma_{i/s/o}^l$  for which formula (8) defining  $\begin{bmatrix} \mathfrak{A}_l(z) & \mathfrak{B}_l(z) \\ \mathfrak{C}_l(z) & \mathfrak{D}_l(z) \end{bmatrix}$  makes sense.

## Right and Left Generalized Transfer Functions Are Well Defined

**Theorem 1.** The right and left generalized four block transfer functions defined by (7) and (8), respectively, do not depend on the choice of  $\Sigma_{i/s/o}^r$  and  $\Sigma_{i/s/o}^l$ , as long as  $z \in \Omega(\Sigma_{i/s/o}^r)$  or  $z \in \Omega(\Sigma_{i/s/o}^l)$ .

## Right and Left Generalized Transfer Functions Coincide

**Theorem 2.** The right and left generalized four block transfer functions defined by (7) and (8), respectively, coincide on

$$\Omega(\Sigma; \mathcal{U}, \mathcal{Y}) = \Omega^r(\Sigma; \mathcal{U}, \mathcal{Y}) \cap \Omega^l(\Sigma; \mathcal{U}, \mathcal{Y})$$

(whenever this set is nonempty).

## Right and Left Generalized Transfer Functions Coincide

**Theorem 2.** The right and left generalized four block transfer functions defined by (7) and (8), respectively, coincide on

$$\Omega(\Sigma; \mathcal{U}, \mathcal{Y}) = \Omega^r(\Sigma; \mathcal{U}, \mathcal{Y}) \cap \Omega^l(\Sigma; \mathcal{U}, \mathcal{Y})$$

(whenever this set is nonempty).

The decomposition is admissible if and only if  $0 \in \Omega(\Sigma; \mathcal{U}, \mathcal{Y})$ .

## Right and Left Generalized Transfer Functions Coincide

**Theorem 2.** The right and left generalized four block transfer functions defined by (7) and (8), respectively, coincide on

$$\Omega(\Sigma; \mathcal{U}, \mathcal{Y}) = \Omega^r(\Sigma; \mathcal{U}, \mathcal{Y}) \cap \Omega^l(\Sigma; \mathcal{U}, \mathcal{Y})$$

(whenever this set is nonempty).

The decomposition is admissible if and only if  $0 \in \Omega(\Sigma; \mathcal{U}, \mathcal{Y})$ .

If the decomposition  $\mathcal{W} = \mathcal{Y} \dot{+} \mathcal{U}$  is admissible, and if  $A$  is the main operator of the corresponding i/s/o representation of  $\Sigma$ , then

$$\Omega^r(\Sigma; \mathcal{U}, \mathcal{Y}) = \Omega^l(\Sigma; \mathcal{U}, \mathcal{Y}) = \Lambda_A,$$

and the right and left generalized four block transfer functions coincide with the ordinary four block transfer function corresponding to the decomposition  $\mathcal{W} = \mathcal{Y} \dot{+} \mathcal{U}$ .

# Coprime Representations

# Stable I/S/O Systems

## Stable I/S/O Systems

- An i/s/o system  $\Sigma_{i/s/o} = ([\begin{smallmatrix} A & B \\ C & D \end{smallmatrix}]; \mathcal{X}, \mathcal{U}, \mathcal{Y})$  is stable if the trajectories  $(x(\cdot), u(\cdot), y(\cdot))$  of this system has the property that  $x(\cdot) \in \ell^\infty(\mathcal{X})$  and  $y(\cdot) \in \ell^2(\mathcal{Y})$  whenever  $u(\cdot) \in \ell^2(\mathcal{U})$ .

## Stable I/S/O Systems

- An i/s/o system  $\Sigma_{i/s/o} = ([\begin{smallmatrix} A & B \\ C & D \end{smallmatrix}]; \mathcal{X}, \mathcal{U}, \mathcal{Y})$  is stable if the trajectories  $(x(\cdot), u(\cdot), y(\cdot))$  of this system has the property that  $x(\cdot) \in \ell^\infty(\mathcal{X})$  and  $y(\cdot) \in \ell^2(\mathcal{Y})$  whenever  $u(\cdot) \in \ell^2(\mathcal{U})$ .
- A right or left affine i/s/o representation is stable if it is stable when regarded as an i/s/o system.

## Stable I/S/O Systems

- An i/s/o system  $\Sigma_{i/s/o} = ([\begin{smallmatrix} A & B \\ C & D \end{smallmatrix}]; \mathcal{X}, \mathcal{U}, \mathcal{Y})$  is stable if the trajectories  $(x(\cdot), u(\cdot), y(\cdot))$  of this system has the property that  $x(\cdot) \in \ell^\infty(\mathcal{X})$  and  $y(\cdot) \in \ell^2(\mathcal{Y})$  whenever  $u(\cdot) \in \ell^2(\mathcal{U})$ .
- A right or left affine i/s/o representation is stable if it is stable when regarded as an i/s/o system.
- The main operator  $A$  of a stable system has the property that  $\mathbb{D} \subset \Lambda_A$  and that its i/o transfer function belongs to  $H^\infty$  over the unit disk  $\mathbb{D}$ . (This applies also to right and left affine i/s/o representations.)

## Stabilizable and Detectable S/S Systems

A s/s system  $\Sigma$  is

# Stabilizable and Detectable S/S Systems

A s/s system  $\Sigma$  is

- **stabilizable** if it has a **stable right affine** i/s/o representation,

## Stabilizable and Detectable S/S Systems

A s/s system  $\Sigma$  is

- **stabilizable** if it has a **stable right affine** i/s/o representation,
- **detectable** if it has a **stable left affine** i/s/o representation,

## Stabilizable and Detectable S/S Systems

A s/s system  $\Sigma$  is

- **stabilizable** if it has a **stable right affine** i/s/o representation,
- **detectable** if it has a **stable left affine** i/s/o representation,
- **LFT-stabilizable** if it has a **stable i/s/o** representation. (LFT = Linear Fractional Transformation.)

## Stabilizable and Detectable S/S Systems

A s/s system  $\Sigma$  is

- **stabilizable** if it has a **stable right affine** i/s/o representation,
- **detectable** if it has a **stable left affine** i/s/o representation,
- **LFT-stabilizable** if it has a **stable i/s/o** representation. (LFT = Linear Fractional Transformation.)

Every **LFT-stabilizable system** is both **stabilizable and detectable**, since an i/s/o representation of a s/s system can be interpreted both as a left affine and as a right affine i/s/o representation of this system.

# Stabilizable and Detectable S/S Systems

A s/s system  $\Sigma$  is

- **stabilizable** if it has a **stable right affine** i/s/o representation,
- **detectable** if it has a **stable left affine** i/s/o representation,
- **LFT-stabilizable** if it has a **stable i/s/o** representation. (LFT = Linear Fractional Transformation.)

Every **LFT-stabilizable system** is both **stabilizable and detectable**, since an i/s/o representation of a s/s system can be interpreted both as a left affine and as a right affine i/s/o representation of this system.

In particular, every s/s system which is **passive** in the sense of [AS06a] is **LFT-stabilizable**.

## Generalized Transfer Functions In $H^\infty/H^\infty$

Recall: the right or left i/o transfer functions of **stable affine representations** are defined in the full unit disk  $\mathbb{D}$ , and they **belong to  $H^\infty$  over  $\mathbb{D}$** .

## Generalized Transfer Functions In $H^\infty/H^\infty$

Recall: the right or left i/o transfer functions of **stable affine representations** are defined in the full unit disk  $\mathbb{D}$ , and they **belong to  $H^\infty$  over  $\mathbb{D}$** .

Thus, if  $\Sigma$  is **stabilizable**, then to every direct sum decomposition  $\mathcal{W} = \mathcal{Y} \dot{+} \mathcal{U}$  of  $\mathcal{W}$  (admissible or not) we obtain a **generalized right i/o transfer function** (from  $\mathcal{U}$  to  $\mathcal{Y}$ ) defined as a (formal) **right fraction**  $\mathfrak{D}_r(z) = \mathfrak{D}'_{\mathcal{Y}}(z)\mathfrak{D}'_{\mathcal{U}}(z)^{-1} \in H^\infty(\mathbb{D})/H^\infty(\mathbb{D})$ .

## Generalized Transfer Functions In $H^\infty/H^\infty$

Recall: the right or left i/o transfer functions of **stable affine representations** are defined in the full unit disk  $\mathbb{D}$ , and they **belong to  $H^\infty$  over  $\mathbb{D}$** .

Thus, if  $\Sigma$  is **stabilizable**, then to every direct sum decomposition  $\mathcal{W} = \mathcal{Y} \dot{+} \mathcal{U}$  of  $\mathcal{W}$  (admissible or not) we obtain a **generalized right i/o transfer function** (from  $\mathcal{U}$  to  $\mathcal{Y}$ ) defined as a (formal) **right fraction**  $\mathfrak{D}_r(z) = \mathfrak{D}'_{\mathcal{Y}}(z)\mathfrak{D}'_{\mathcal{U}}(z)^{-1} \in H^\infty(\mathbb{D})/H^\infty(\mathbb{D})$ .

If  $\Sigma$  is **detectable**, then to every direct sum decomposition  $\mathcal{W} = \mathcal{Y} \dot{+} \mathcal{U}$  of  $\mathcal{W}$  we obtain a **generalized left i/o transfer function** defined as a (formal) **left fraction**  $\mathfrak{D}_r(z) = \mathfrak{D}''_{\mathcal{Y}}(z)^{-1}\mathfrak{D}''_{\mathcal{U}}(z) \in H^\infty(\mathbb{D}) \setminus H^\infty(\mathbb{D})$ .

## Generalized Transfer Functions In $H^\infty/H^\infty$

Recall: the right or left i/o transfer functions of **stable affine representations** are defined in the full unit disk  $\mathbb{D}$ , and they **belong to  $H^\infty$  over  $\mathbb{D}$** .

Thus, if  $\Sigma$  is **stabilizable**, then to every direct sum decomposition  $\mathcal{W} = \mathcal{Y} \dot{+} \mathcal{U}$  of  $\mathcal{W}$  (admissible or not) we obtain a **generalized right i/o transfer function** (from  $\mathcal{U}$  to  $\mathcal{Y}$ ) defined as a (formal) **right fraction**  $\mathfrak{D}_r(z) = \mathfrak{D}'_{\mathcal{Y}}(z)\mathfrak{D}'_{\mathcal{U}}(z)^{-1} \in H^\infty(\mathbb{D})/H^\infty(\mathbb{D})$ .

If  $\Sigma$  is **detectable**, then to every direct sum decomposition  $\mathcal{W} = \mathcal{Y} \dot{+} \mathcal{U}$  of  $\mathcal{W}$  we obtain a **generalized left i/o transfer function** defined as a (formal) **left fraction**  $\mathfrak{D}_r(z) = \mathfrak{D}''_{\mathcal{Y}}(z)^{-1}\mathfrak{D}''_{\mathcal{U}}(z) \in H^\infty(\mathbb{D}) \setminus H^\infty(\mathbb{D})$ .

If  $\Sigma$  is **LFT-stabilizable**, then these generalized right and left affine i/o transfer functions are even **right or left coprime in  $H^\infty(\mathbb{D})$** , respectively.

# Generalized Nevanlinna and Potapov Class Functions

By applying our theory to passive  $s/s$  systems we obtain right and left coprime transmission representations of these systems.

## Generalized Nevanlinna and Potapov Class Functions

By applying our theory to **passive s/s systems** we obtain **right and left coprime transmission representations** of these systems.

In the case where the positive and negative dimensions of the signal space  $\mathcal{W}$  are the same we also obtain **right and left coprime impedance representations**.

## Generalized Nevanlinna and Potapov Class Functions

By applying our theory to **passive s/s systems** we obtain **right and left coprime transmission representations** of these systems.

In the case where the positive and negative dimensions of the signal space  $\mathcal{W}$  are the same we also obtain **right and left coprime impedance representations**.

The corresponding right and left coprime affine i/o transfer functions will be **generalized Potapov and Nevanlinna class functions (relations)**, respectively.

## Unbounded Impedance Representations

It is also possible to give an unbounded i/s/o impedance representation of a passive s/s system in the case where the impedance function is single-valued, but the values are unbounded maximal accretive operators.

In this representation the bounded block operator  $\begin{bmatrix} A & B \\ C & D \end{bmatrix}$  is replaced by an unbounded operator, and the theory resembles the continuous time system node theory presented in [Sta05].

The main difference is that the “inside” (the state space  $\mathcal{X}$ ) and the “outside” (the common input and output space) have changed places.

## Details

For details, see [\[AS06b\]](#), [\[AS06c\]](#), and [\[Sta06\]](#).

## References

- [AS05] Damir Z. Arov and Olof J. Staffans, [State/signal linear time-invariant systems theory. Part I: Discrete time systems](#), The State Space Method, Generalizations and Applications (Basel Boston Berlin), Operator Theory: Advances and Applications, vol. 161, Birkhäuser-Verlag, 2005, pp. 115–177.
- [AS06a] \_\_\_\_\_, [State/signal linear time-invariant systems theory. Passive discrete time systems](#), Internat. J. Robust Nonlinear Control **16** (2006), 52 pages, Manuscript available at <http://www.abo.fi/~staffans/>.
- [AS06b] \_\_\_\_\_, [State/signal linear time-invariant systems theory. Part III: Transmission and impedance representations of discrete time systems](#), Submitted in May, 2006.
- [AS06c] \_\_\_\_\_, [State/signal linear time-invariant systems theory. Part IV: Affine representations of discrete time systems](#), In preparation, 2006.

- [Sta05] Olof J. Staffans, [Well-posed linear systems](#), Cambridge University Press, Cambridge and New York, 2005.
- [Sta06] \_\_\_\_\_, [Passive linear discrete time-invariant systems](#), Proceedings of ICM2006, Madrid, 2006.